

IN THE ABSTRACT:

Please substitute the following Abstract for the originally filed Abstract:

A digital power amplifier has first and second switches, between a high potential power line and a low potential power line, which operate complementarily. When switching an ON switch, dead time is provided to control power to a low-pass filter. In a switching section, the first switch, a first coil, a second coil and the second switch are connected in series, between the high and low potential power lines. The switching section includes a first high-speed diode having a cathode connected to the high potential power line and an anode connected to a node between the second coil and the second switch, and a second high-speed diode having a cathode connected to a node between the first switch and the first coil and an anode connected to the low potential power line. The node between the first coil and the second coil is connected to the low-pass filter side.